

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jishnu Bhattacharjee; Madabusi Govindarajan; Debanjan Mukherjee;
Abhijit Phanse, Prashant Choudhary
Assignee: Scintera Networks, Inc.
Title: Compact, High Q Inductor For Integrated Circuit
Serial No.: 10/718,501 Filing Date November 19, 2003
Examiner: Unassigned Group Art Unit: Unassigned
Docket No.: M-15260 US

Irvine, California

COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

VERIFIED STATEMENT OF FACTS BY NAMED INVENTOR

Dear Sir:

Jishnu Bhattacharjee declares that:

I, Jishnu Bhattacharjee, am an inventor of the subject matter claimed in the above referenced patent application. I hereby agree to the addition of Prashant Choudhary as a named inventor.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful, false statements may jeopardize the validity of the application or any patent issued thereon.

LAW OFFICES OF
MACPHERSON KWOK CHEN
& BEID LLP

2400 MICHELSON DRIVE
SUITE 210
IRVINE, CA 92612
(949) 752-7040
FAX (949) 752-7049

Date: 03/11/2007

Jishnu Bhattacharjee
Jishnu Bhattacharjee

Appendix A

-1-

Serial No. 10/718,501

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jishnu Bhattacharjee; Madabusi Govindarajan; Debanjan Mukherjee;
Abhijit Phanse, Prashant Choudhary
Assignee: Scintera Networks, Inc.
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Irvine, California

COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

VERIFIED STATEMENT OF FACTS BY NAMED INVENTOR

Dear Sir:

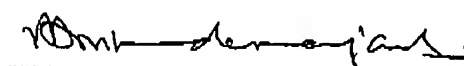
Madabusi Govindarajan declares that:

I, Madabusi Govindarajan, am an inventor of the subject matter claimed in the above referenced patent application. I hereby agree to the addition of Prashant Choudhary as a named inventor.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful, false statements may jeopardize the validity of the application or any patent issued thereon.

LAW OFFICES OF
MACPHERSON KWOK CHEN
& HEID LLP

2402 MICHELSON DRIVE
SUITE 210
IRVINE, CA 92612
(949) 732-7040
FAX (949) 733-0057

Date: March 23, 2004

Madabusi Govindarajan

Appendix B

ATTESTED BY ME



B. ANANTHA

ADVOCATE & NOTARY

-1-

Serial No. 10/718,501



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jishnu Bhattacharjee; Madabusi Govindarajan; Debanjan Mukherjee;
Abhijit Phanse, Prashant Choudhary
Assignee: Scintera Networks, Inc.
Title: Compact, High Q Inductor For Integrated Circuit
Serial No.: 10/718,501 Filing Date November 19, 2003
Examiner: Unassigned Group Art Unit: Unassigned
Docket No.: M-15260 US

Irvine, California

COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

VERIFIED STATEMENT OF FACTS BY NAMED INVENTOR

Dear Sir:

Debanjan Mukherjee declares that:

I, Debanjan Mukherjee, am an inventor of the subject matter claimed in the above referenced patent application. I hereby agree to the addition of Prashant Choudhary as a named inventor.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful, false statements may jeopardize the validity of the application or any patent issued thereon.

Date: 03/11/04

Debanjan Mukherjee
Debanjan Mukherjee

Appendix C

-1-

Serial No. 10/718,501

LAW OFFICES OF
MACPHERSON KWOK CHEN
& HEND LLP

2405 MICHELSON DRIVE
SUITE 210
IRVINE, CA 92612
(949) 752-7040
FAX (949) 752-7049

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jishnu Bhattacharjee; Madabusi Govindarajan; Debanjan Mukherjee;
Abhijit Phanse, Prashant Choudhary
Assignee: Scintera Networks, Inc.
Title: Compact, High Q Inductor For Integrated Circuit
Serial No.: 10/718,501 Filing Date November 19, 2003
Examiner: Unassigned Group Art Unit: Unassigned
Docket No.: M-15260 US

Irvine, California

COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

VERIFIED STATEMENT OF FACTS BY NAMED INVENTOR

Dear Sir:

Abhijit Phanse declares that:

I, Abhijit Phanse, am an inventor of the subject matter claimed in the above referenced patent application. I hereby agree to the addition of Prashant Choudhary as a named inventor.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful, false statements may jeopardize the validity of the application or any patent issued thereon.

LAW OFFICES OF
MACPHERSON KWOK CHEN
& HEID LLP

2402 MICHELSON DRIVE
SUITE 210
IRVINE, CA 92612
(949) 752-7040
FAX (949) 752-7049

Date: March 11, 2004

APh
Abhijit Phanse

Appendix D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jishnu Bhattacharjee; Madabusi Govindarajan; Debanjan Mukherjee;
Abhijit Phanse, Prashant Choudhary
Assignee: Scintera Networks, Inc.
Title: Compact, High Q Inductor For Integrated Circuit
Serial No.: 10/718,501 Filing Date November 19, 2003
Examiner: Unassigned Group Art Unit: Unassigned
Docket No.: M-15260 US

Irvine, California

COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

VERIFIED STATEMENT OF FACTS BY UNNAMED INVENTOR

Dear Sir:

Prashant Choudhary declares that:

I declare that all statements made herein of my own knowledge are true and that I, Prashant Choudhary, am an inventor of the subject matter claimed in the above-referenced patent application. The omission of my name as an inventor was an error that occurred without any deceptive intent on my part.

I hereby declare that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful, false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful, false statements may jeopardize the validity of the application or any patent issued thereon.

Date: 03/11/2004


Prashant Choudhary

Appendix E

-1-

Serial No. 10/718,501

LAW OFFICES OF
MACPHERSON KWOK CHEN
& REID LLP

2402 MICHELSON DRIVE
SUITE 210
IRVINE, CA 92614
(949) 752-7040
FAX (949) 752-7049

**SUPPLEMENTAL DECLARATION FOR PATENT APPLICATION
AND POWER OF ATTORNEY**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below adjacent to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of subject matter (process, machine, manufacture, or composition of matter, or an improvement thereof) which is claimed and for which a patent is sought by way of the application entitled

COMPACT, HIGH Q INDUCTOR FOR INTEGRATED CIRCUIT

which (check) ☐ is attached hereto.
☐ and is amended by the Preliminary Amendment attached hereto.
☒ was filed on November 19, 2003 as Application Serial No. 10/718,501
☐ and was amended on (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information, which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) of any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application

n(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed:

Prior Foreign Application(s)			Priority Claimed	
Number	Country	Day/Month/Year Filed	Yes	No
			<input type="checkbox"/>	<input type="checkbox"/>

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:

Provisional Application Number	Filing Date

Appendix F

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) or PCT international application(s) designating the United States of America listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior application(s) in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information, which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56, which became available between the filing date of the prior application(s) and the national or PCT international filing date of this application:

Application Serial No.	Filing Date	Status (patented, pending, abandoned)

I hereby appoint the following practitioners to prosecute this application and to transact all business in the United States Patent and Trademark Office connected therewith:

Customer No. 32605

Please address all correspondence and telephone calls to:

Theodore P. Lopez
MacPHERSON KWOK CHEN & HEID LLP
 1762 Technology Drive, Suite 226
 San Jose, California 95110
 Telephone: 949-752-7040
 Facsimile: 408-392-9262

I declare that all statements made herein of my own knowledge are true, all statements made herein on information and belief are believed to be true, and all statements made herein are made with the knowledge that whoever, in any matter within the jurisdiction of the Patent and Trademark Office, knowingly and willfully falsifies, conceals, or covers up by any trick, scheme, or device a material fact, or makes any false, fictitious or fraudulent statements or representations, or makes or uses any false writing or document knowing the same to contain any false, fictitious or fraudulent statement or entry, shall be subject to the penalties including fine or imprisonment or both as set forth under 18 U.S.C. 1001, and that violations of this paragraph may jeopardize the validity of the application or this document, or the validity or enforceability of any patent, trademark registration, or certificate resulting therefrom.

Full name of first joint inventor:		Jishnu Bhattacharjee	
Inventor's Signature:	<i>Jishnu Bhattacharjee</i>	Date:	03/11/2004
Residence:	San Jose, California		
Post Office Address:	4337 Norwalk Drive, Apt. #N-184	Citizenship:	India
	San Jose, California 95129		
Full name of second joint inventor:		Madabusi Govindarajan	
Inventor's Signature:		Date:	
Residence:	Sunnyvale, California		
Post Office Address:	1697 Canary Drive	Citizenship:	India
	Sunnyvale, California 95129		

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) or PCT international application(s) designating the United States of America listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior application(s) in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose information, which is material to patentability as defined in Title 37, Code of Federal Regulations, § 1.56, which became available between the filing date of the prior application(s) and the national or PCT international filing date of this application:

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I declare that all statements made herein of my own knowledge are true, all statements made herein on information and belief are believed to be true, and all statements made herein are made with the knowledge that whoever, in any matter within the jurisdiction of the Patent and Trademark Office, knowingly and willfully falsifies, conceals, or covers up by any trick, scheme, or device a material fact, or makes any false, fictitious or fraudulent statements or representations, or makes or uses any false writing or document knowing the same to contain any false, fictitious or fraudulent statement or entry, shall be subject to the penalties including fine or imprisonment or both as set forth under 18 U.S.C. 1001, and that violations of this paragraph may jeopardize the validity of the application or this document, or the validity or enforceability of any patent, trademark registration, or certificate resulting therefrom.

Full name of first joint inventor:		Jishnu Bhattacharjee	
Inventor's Signature:		Date:	
Residence:	San Jose, California		
Post Office Address:	4337 Norwalk Drive, Apt. #N-184	Citizenship:	India
	San Jose, California 95129		
Full name of second joint inventor:		Madabusi Govindarajan	
Inventor's Signature:	<i>Madabusi Govindarajan</i>	Date:	23 Mar 2004
Residence:	Sunnyvale, California		
Post Office Address:	1697 Canary Drive	Citizenship:	India
	Sunnyvale, California 95129		

ATTESTED BY *B. Anantha* 23-3-04

B. Anantha

- Page 2 of 3 -

B. ANANTHA
 ADVOCATE & NOTARY

No. 96/2, Murugesh Mudaliyar Lane
 (Behind Multi Silk House)
 11, Anna Masjid Road, Coimbatore



Full name of third joint inventor:		Debanjan Mukherjee	
Inventor's Signature:	<i>Debanjan Mukherjee</i>	Date:	03/11/04
Residence:	San Jose, California		
Post Office Address:	4337 Norwalk Drive, Apt. #N-184	Citizenship:	India
	San Jose, California 95129		
Full name of fourth joint inventor:		Abhijit Phanse	
Inventor's Signature:	<i>Abhijit Phanse</i>	Date:	3/11/04
Residence:	Sunnyvale, California		
Post Office Address:	655 S. Fair Oaks Avenue, Apt. H214	Citizenship:	India
	Sunnyvale, California 94086		
Full name of fifth joint inventor:		Prashant Choudhary	
Inventor's Signature:	<i>Prashant Choudhary</i>	Date:	03/11/04
Residence:	San Jose, California		
Post Office Address:	4231 Norwalk Drive	Citizenship:	India
	San Jose, California 95129		

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Assignee: Scintera Networks, Inc.
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Docket No.: M-15260 US

Irvine, California

COMMISSIONER FOR PATENTS
Alexandria, VA 22313-1450

CONSENT BY ASSIGNEE TO ADD INVENTOR

Dear Sir:

Scintera Networks, Inc., Assignee of the above-identified application in the names of Jishnu Bhattacharjee; Madabusi Govindarajan; Debanjan Mukherjee, and Abhijit Phanse, hereby consent to the addition of Prashant Choudhary as a named co-inventor in the above-referenced application.

Scintera Networks, Inc.

Dated: 04.26.04By: Abhijit Phanse

Title: Vice President, Founder, Director
& Chief

Address: 4340 Stevens Creek
Suite 260
San Jose, CA 95129

Appendix G

LAW OFFICES OF
MACPHERSON KWOK CHEN
& ASSOCIATES

2000 MICHELLESON DRIVE
SUITE 200
REDWOOD CITY, CA 94063
(650) 750-0000
FAX (650) 750-7000